



General Chair

Katarína Jelemenská, STU Bratislava, SK

General Vice-Chair

Alberto Bosio, Ecole Centrale Lyon, INL, FR

Program Co-Chairs

Lukáš Sekanina, VUT Brno, CZ
Rolf Drechsler, University of Bremen, DE

Publication Co-Chairs

Viera Stopjaková, STU Bratislava, SK
Lukáš Nagy, STU Bratislava, SK

Web Chair

Michal Šovčík, STU Bratislava, SK

Local Chair

Miroslava Slezáková, STU Bratislava, SK

Financial Chair

Eva Šoltésová, STU Bratislava, SK

Steering Committee

W. Pleskacz, chair

A. Bosio
G. Cserey
S. Deniziak
P. Fišer
T. Garbolino
M. Jenihhin
K. Jelemenská
L. Miclea
J. Raik
M. Schölzel
L. Sekanina
Z. Stamenkovic
A. Steininger
V. Stopjaková

Program Committee

S. Abrar
H. Amrouch
M. Arafin
D. Arbet
F. Azais
M. Barragan
J. Belohoubek
A. Bernasconi
A. Bosio
L. Cassano
S. Chakraborty
M. Danek
S. Deniziak
R. Drechsler
M. Drutarovsky
S. Eggersglüß
P. Ellervee
G. Fey
M. Fieback
P. Fišer
T. Garbolino
J. Han
M. Hasan Ahmadilivani
S. Hellebrand
M. Hofbauer
F. Huemer
G. Iaria
K. Jelemenská
M. Jenihhin
D. Kasprovicz
E. Bilge Kavun
M. Keim
O. Keszöcze
M. Krstic
H. Kubátová
E. Larsson
G. Leger
R. Leveugle
J. Machado da Silva
P. Maistri
V. Melikyan
L.-C. Miclea
L. Nagy
S. Pagliarini
Z. Peng
L. Pierre
W. Pleskacz
I. Polian
T. Polzer
M. Portolan
J. Raik
M. Rawski
F. Regazzoni
R. Ruzicka
E. Sanchez
M. Schölzel
L. Sekanina
P. Sitek
M. Sonza Reorda
J. Sosnowski
R. Šotner
Z. Stamenkovic
A. Steininger
V. Stopjaková
O. Subrt
M. Traiola
M. Uderscu
M. Ulbricht
F. Luis Vargas
T. Vayssade
D. Vazquez
T. Villa
S. Vinco
A. Virazel
J. Vliegen
H.-J. Wunderlich

Call for Papers

The IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems provides a forum for exchanging ideas, discussing research results, and presenting practical applications in the areas of design, test and diagnosis of electronic circuits and systems.

Topics

Topics of interest include the following main areas (but not limited to):

Digital Circuit Design and Next Generation Chips

Chair:
Sara Vinco
Politecnico di Torino, IT

Verification and Test

Chair:
Georschwin Fey
Hamburg University of Technology, DE

Analog Circuit Design and Test

Chair:
Viera Stopjaková,
FEI STU Bratislava, SK

Emerging Technologies and Security

Chair:
Jie Han
University of Alberta, CA

Submission Instructions

Prospective authors are cordially invited to submit original papers using the electronic submission system at the Symposium web page. Papers in English following the IEEE conference style are expected.

Types of submissions:

- **Regular Papers** (6 pages) presenting novel and completed research work.
- **Student Papers** (4 pages) presenting novel, promising research results; the first author must be a student.
- **Embedded tutorials, Special Sessions** (3 pages for the proposal including the title, extended abstract, and speakers' short bios). Preparing a paper for accepted tutorials and special sessions is optional. For accepted special sessions, speakers can create a joint paper (up to 8 pages). Another option is to submit individual contributions as regular papers that will undergo the standard review process. Accepted papers will be presented in the same session.

DDECS 2026 will produce Formal Proceedings of accepted papers that will be submitted for possible inclusion in the IEEE Xplore Digital Library.

Important Dates

Paper submission:	41 January 2026	25 January 2026
Notification of acceptance:	27 February 2026	9 March 2026
Camera-ready:	16 March 2026	30 March 2026



Conference Location

Bratislava is the capital and largest city of Slovakia and the political, cultural and economic centre of the country. It is the seat of the Slovak president, the Parliament and the Slovak Executive. It has several universities, and many museums, theatres, galleries and other cultural and educational institutions.

Main Contacts

Katarína Jelemenská: katarina.jelemenska@stuba.sk
Lukáš Sekanina: sekanina@fit.vutbr.cz

